

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : John A. Kielb

Filed : Herewith

For : STABILIZATION IN A RADAR
LEVEL GAUGE

Docket No.: R11.12-0662

1c584 U.S. PTO
09/347683

07/02/99

TRANSMITTAL LETTER

"Express Mail" mailing label number: **EL229535299US**
Date of Deposit: **July 2, 1999**

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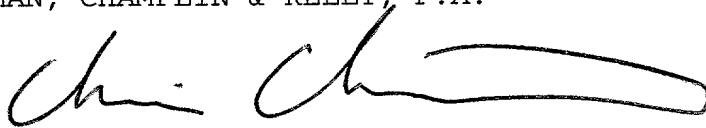
1. Our checks in the amount of \$838.00 and \$40.00
2. Fee Calculation Sheet (in duplicate)
3. Patent Application comprising the following pages:
 - 1 Abstract
 - 13 Specification
 - 6 Claims
4. 8 Sheets of drawings
5. Executed Declaration and Power of Attorney (2 pages)
6. Executed Assignment and Recordation Form Cover Sheet

Under 37 CFR § 1.136(a)(3), applicant(s) hereby authorize(s) for any future reply, the incorporation of any required petition for extension of time for the appropriate length of time and authorize the charging of fees under § 1.17 to deposit account 23-1123.

Respectfully submitted,

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EL229535299US

PATENT APPLICATION OF

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ENTITLED

STABILIZATION IN A RADAR LEVEL GAUGE

Docket No. R11.12-0662

STABILIZATION IN A RADAR LEVEL GAUGE

BACKGROUND OF THE INVENTION

Pulsed time-of-flight radar gauges are utilized for the measurement of level in process storage tanks. These gauges are mounted on the top of storage tanks, and transmit a pulse of microwave energy toward the surface of the product being stored in the tank. The gauge then receives energy which is reflected from the surface of the product, and calculates the level of the product based upon the time of flight of the pulse.

A PRIOR ART circuit 110 for creating the transmit and sample clocks is shown in Figure 2. Circuits of this type are described in U.S. Patent 5,563,605 McEwan. This circuit has the advantage that only one oscillator 112 is required, as the sample clock 114 is generated by continuously increasing phase delay in a variable delay generator 116 controlled by a delay control ramp 118. The phase delay circuit can be designed to be repeatable, therefore, errors due to changes in the difference frequency are reduced using this circuit design. However, this circuit has the disadvantage of having significant phase jitter or instability in the sample clock. This is a result of performance limitations of the high speed comparator required as part of the phase delay generator.

There is a need for a radar gauge circuit that is stabilized without the use of expensive, complex circuitry.

SUMMARY OF THE INVENTION

In the present invention, a controller feeds back a control output to a clock source. The feedback

stabilizes a first frequency separation between first and second clock frequencies generated by the clock source. A separation sensing circuit is coupled to the clock source and generates an evaluation output as a function of the first frequency separation. The evaluation outputs is coupled to the controller for controlling the control output. A radar gauge circuit receives the first or transmit frequency and the second or sample frequency and controls radar transmission and level sampling as a function of the transmit and sampling frequencies. The radar gauge circuit generates a level output that is stabilized and corrected as a function of the frequency separation.

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BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 shows a first block diagram of a radar level gauge.

FIG. 2 shows PRIOR ART radar level gauge.

20 FIG. 3 shows a typical installation environment for a radar level gauge.

FIG. 4 shows an exploded view of a radar level gauge.

25 FIG. 5 shows a second block diagram of a radar level gauge with a divider circuit.

FIG. 6 shows a third block diagram of a radar level gauge with dividers.

FIG. 7 shows a fourth block diagram of a radar level gauge.

30 FIG. 8 shows a circuit diagram of an evaluation circuit for a radar level gauge.

DETAILED DESCRIPTION OF THE EMBODIMENTS

In FIG. 1, a clock control circuit 100 is shown that can be used in a radar gauge circuit (described later in connection with FIG. 7) to sense fluid level in a tank. The radar gauge circuit of FIG. 7 is adapted to receive a transmit frequency on line 126 and a sample frequency on line 128 controlling radar transmission and level sampling respectively. The radar gauge circuit generating a level output at 202 in FIG. 7.

In FIG. 1, a clock source comprises a system clock 106 and a VCO 104. The clock source generates a first clock frequency on line 107 and a second clock frequency on line 105. The difference between the first clock frequency and the second clock frequency is called a first frequency separation. The clock source has a control input at 109 setting a first frequency separation between the first and second clock frequencies, by controlling the VCO frequency. A separation sensing circuit, or difference frequency detector 111 is coupled to the clock source and generates an evaluation output 113 as a function of the first frequency separation. A controller 102, which can be a microprocessor system, receives the evaluation output 113. The controller has a timer 115 that measures the first frequency separation. The controller generates a control output 116 that feeds back to the control input 109 via D/A converter 108. The feedback stabilizes the first separation as a function of timing the evaluation output 113 with timer 115. The controller 102 further has a correction circuit 117, preferably implemented in software, that corrects the level output as a function of the first frequency separation.

The circuit 100 in FIG. 1 utilizes a microprocessor 102 in the feedback loop to control the frequency of the VCO 104 to be offset by a fixed difference from the frequency of the system clock 106.

5 In this circuit, the difference frequency between the two oscillators 104, 106 is precisely controlled. Since both the system clock 106 and the VCO 104 are continuously drifting in time, it is difficult to keep them locked to a very precise offset frequency. Also, 10 the measurement of distance begins when the system clock 106 and sample clock 104 are in phase. Therefore, circuitry is used to detect when these two oscillators 104, 106 are in phase. The voltage of the D/A 108 is fixed during each measurement. This means 15 that the oscillators will typically be drifting only slightly away from the exact difference frequency during the distance measurement. This results in improved stability in the distance measurement.

FIG. 3 shows a schematic representation of a 20 process tank 10 in which a microwave radar level gauge is installed according to the present invention. Process tank 10 is filled with a liquid 12, the height or level of which is to be determined by radar level gauge 14. The radar level gauge 14 is mounted on a 25 tank port 16 at the top of the tank, and is sealed relative to the tank. The radar level gauge 14 transmits microwave energy along a waveguide, through port 16 and receives reflected energy from the liquid surface 13 to provide an indication of the level of 30 the liquid within the tank.

Radar level gauge 14 includes an electronics housing 20, a housing to flange adapter 22, a process connecting flange 24 and an antenna 26. Radar level

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gauge 14 is mounted to a standoff pipe 28 which is fastened to the top of tank 10, around port 16. Tank flange 30 is fastened to standoff pipe 28. Process connecting flange 24 is secured with bolts to tank flange 30 and is sealed with a suitable gasket. Process connecting flange 24 supports both adapter 22 and electronics housing 20.

FIG. 4 is an exploded perspective view of radar level gauge 14. Electronics housing 20 includes a terminal compartment 32 and an electronics compartment 34 which are separated by a barrier 36. A terminal block 38 is mounted within terminal compartment 32 for providing external electrical connections to the electronics within electronics compartment 34. The external electrical connections are used to couple radar level gauge 14 to a process control link, such as a two or four wire process control loop or a process control bus, over which radar level gauge 14 receives power and signal information and transmits level measurements and other status information. A cover 40 is threaded into terminal compartment 32 for sealing the terminal compartment with respect to the outside environment. The electronics that are used for transmitting microwave energy, receiving the reflections and for making sensor calculations are provided on a stack of printed circuit boards 42 which are mounted within electronics compartment 34. The electronics are well known in the industry. A cover 44 is threaded into electronics compartment 34 for sealing the electronics compartment with respect to the outside environment.

The electronics, comprised of the stack of printed circuit boards 42, provide microwave energy

through a coaxial cable (coax) connection 46 which is coupled to a coaxial to rectangular waveguide adapter 48, positioned within electronics housing 20. The coax to rectangular waveguide adapter 48 is secured with screws to a raised boss 50 of housing to flange adapter 22. A waveguide aperture 52 extends through adapter 22 for transmitting the microwave energy to and from antenna 26. Adapter 22 includes a mounting plate 54, which is secured to a lower housing flange 56 of electronics housing 20 with bolts 58. Mounting plate 60 is secured to process mounting flange 24 with bolts 62. Antenna 26 is secured to a lower surface of process connecting flange 24 with bolts 64. Antenna 26 is of conventional design and includes a central aperture at an upper end that aligns with the waveguide aperture 52 in adapter 22 and an aperture 76 through flange 24. Other types of housing and assembly methods can be used for less demanding applications.

In Fig. 5, a block diagram of a radar gauge 120 is shown adapted to sense a fluid level 122 in a tank. Radar gauge 120 includes a radar gauge circuit 124 adapted to receive a transmit frequency 126 and a sample frequency 128 controlling radar transmission 130 and level sampling of fluid level 122 respectively. The radar gauge circuit 124 generates a level output 132 that is corrected by controller 154 for variations in frequency separation. A clock source 134 generates first clock frequency 136 and second clock frequency 138 and has a control input 140 setting a first frequency separation 142 between the first and second clock frequencies. A divider circuit 146 divides the first and second clock frequencies 136, 138 and generates the transmit and the sample

frequencies 126, 128. The transmit and sample frequencies 126, 128 are separated from each other by a second frequency separation 148. A separation sensing circuit 150 couples to the divider circuit 146 and generates evaluation outputs 152 as functions of the first and second frequency separations 142, 148. A controller 154 receives the evaluation outputs 152. The controller 154 has a control output 156 feeding back to the control input 140 that stabilizes the first frequency separation 142 as a function of the evaluation outputs 152. The controller also receives an uncorrected level output on line 153, makes a correction for frequency separation, and provides a corrected level signal on line 155.

In FIG. 6, a block diagram of a further embodiment 160 of a radar gauge is shown. In FIGS. 6-8, reference numerals are used that are the same reference numerals used in FIG. 5 and identify similar or corresponding features to those in FIG. 5. Clock source 134 includes a system clock 160, typically a crystal controlled oscillator that can also be used to provide other clock functions to gage circuit 124. Clock source 134 also includes a voltage controlled oscillator (VCO) 162 that receives a frequency control voltage at control input 140 and generates the second clock frequency 138. Separation sensing circuit 150 includes a first frequency difference detector circuit 164 sensing a first frequency difference between the frequencies 136, 138. Separation sensing circuit 150 also includes a second frequency difference detector circuit 166 sensing a second frequency difference between the frequencies 126, 128. Separation sensing circuit 150 further includes a transmit sample

polarity detector circuit 168 sensing a polarity of the sample clock 128 and generating a further evaluation output 170 representative of the polarity.

In FIG. 6, controller 154 includes a digital-to-analog converter 172 generating the control output 156. Controller 154 includes a timer 174. Timer 174 can measure time intervals of the evaluation outputs 152. The timer 174 also counts the current value of the frequency difference and provides that current value to gauge circuit 124, such that the level output 132 is calculated based on a current value of the frequency difference. This current value takes into account any drifting of the first and second frequencies 136, 138 during the time that the current measurement is being made. Gauge circuit 124 calculates the level output based on this current value so that the output is stabilized as a function of the current timer measurement. Timer 174 can be implemented in hardware, in software running on microprocessor 176, or a combination of hardware and software. Timer 174 can perform a timer measurement of a count of an evaluation output during a selected time interval. Alternatively, a number of counts can be selected, and the time interval for that selected number of counts can be measured.

In FIG. 7, details of the construction of a radar gauge circuit 124 are shown in more detail. Gauge circuit 124 receives transmit and sample clocks 126, 128 as shown from a stabilized clock control 180. Stabilized clock control 180 includes circuitry such as that shown in FIG. 5 at 134, 146, 150, 154. Transmit pulse generator 182 receives transmit clock 126 and controls transmit oscillator 184 which is a

patented high frequency microwave circuit. Transmit oscillator 184 generates a microwave output pulse that is provided to antenna 186 through power splitter 188. Sample pulse generator 190 receives sample clock 128 and controls receive oscillator 192. Receive oscillator 192 provides a reference waveform to pulse receiver 194. Pulse receiver 194 receives a return echo from antenna 186 through power splitter 188. Pulse receiver 194 combines or mixes the reference waveform from receive oscillator 192 and the return echo and generates a lower frequency output on line 196 that is low enough for processing by low cost, low frequency electronic receive amplifier 198. The arrangement using lower frequency circuits such as those shown at 182, 190, 194 to shift microwave measurements to a lower frequency is called equivalent time sampling and is known, for example, from "Smart Transmitter using microwave pulses to measure the level of liquids and solids in process applications" by Hugo Lang and Wolfgang Lubke, Instrument Society of America paper ISA # 93-319 (1993).

The radar gauge circuit 124 includes a transmit pulse generator 182 and a sample pulse generator 190 controlled respectively by the transmit clock 126 and the sample clock 128. The output of the receive amplifier 198 is coupled to A/D converter 206 which converts the amplified signal to a digital form for use by microcontroller 208. Microcontroller 208 calculates the level and provides a level output on line 210 to a 4-20 mA output circuit 212. Output circuit 212 controls the 4-20 mA current energizing the radar level gauge to have an analog value representing the level. Microcontroller 208 utilizes

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In FIG. 8, a circuit diagram of a divider circuit and a separation sensing circuit such as circuit 150

is shown. A system clock frequency is received at 250, and a VCO frequency is received at 252. A first divide by two circuit 254 divides the system clock frequency and generates a transmit clock frequency 126. A second divide by two circuit 256 receives the VCO frequency and generates a sample clock frequency 128. A first frequency difference circuit 258 receives both the system clock frequency and the VCO frequency and generates a first frequency difference output at 260. A second frequency difference circuit 262 receives the transmit clock frequency and the sample clock frequency and generates a second frequency difference output at 264. A polarity sensing circuit 266 senses the polarity of the sample clock relative to the second frequency difference output and generates a polarity, or sign, output at 268. Each of the functions in FIG. 8 can be performed using a low cost type 7474 clocked D flip flop wired as shown in FIG. 8.

The sample polarity detector is connected as a latch that stores the polarity of the sample clock after the leading edge of the transmit clock toggles the Q/ output of the second difference frequency detector. The output of the transmit sample polarity detector is coupled to the microprocessor to indicate whether the sample clock has a lower or higher frequency than the transmit clock. The polarity detector resolves any ambiguity in the absolute value of the frequency difference.

The radar level gauge with stabilization has the advantage of low cost and low phase jitter, while improving overall performance.

The stabilization allows a low cost pulsed

microwave radar measurement to be made with improved performance. The method involves measuring and correcting for the difference between the two critical clock frequencies required in this system, as opposed
5 to trying to precisely generate or control these frequencies.

A timer in the microprocessor counts or times the outputs of the first and second difference frequency detectors. Based on these counts or times, the
10 microprocessor calculates real time data representing the absolute value of the frequency difference between the transmit frequency and the sample frequency. The microprocessor then executes an algorithm that adjusts the control voltage provided to
15 the VCO to maintain the difference frequency in a desired range. The control algorithm in the microprocessor is adjusted so that it does not tightly control the frequency difference, but maintains only limited control within the desired range. The use of
20 limited control rather than tight control of the frequency difference allows low power, low resolution components to be used in the frequency control. Oscillator drift is too fast for the low power, low resolution circuitry to control it, making frequency
25 difference counts somewhat different during each measurement.

The timer is also used to precisely count the somewhat varying difference frequency during the exact time that the distance is being measured. The
30 microprocessor then adjusts the distance calculation based on the actual count of the difference frequency. The timer can be a hardware timer, software implemented in a microprocessor, or a combination of

both. In the microprocessor's algorithm or equation for calculating distance, the frequency difference term ΔF is a real time variable measured by the timer rather than a constant term or a term adjusted only
5 infrequently for compensation.

The combination of limited control of the frequency difference with a precise count of the frequency difference enables the radar gauge to operate with lower noise due to phase jitter in
10 combination with higher accuracy due to precise correction of distance measurement for variations in frequency during the measuring interval and the overall performance of the radar gauge is improved. High phase jitter on the sample clock leads to an
15 unstable equivalent time measurement and instability at level output 132.

Although the present invention has been described with reference to preferred embodiments, workers skilled in the art will recognize that changes may be
20 made in form and detail without departing from the scope of the invention.

WHAT IS CLAIMED IS:

1. A radar gauge adapted to sense fluid level in a tank, comprising:

- a radar gauge circuit adapted to receive a transmit frequency and a sample frequency controlling radar transmission and level sampling respectively, the radar gauge circuit generating a level output;
- a clock source generating first and second clock frequencies and having a control input setting a first frequency separation between the first and second clock frequencies;
- a separation sensing circuit coupled to the clock source and generating an evaluation output as a function of the first frequency separation; and
- a controller receiving the evaluation output, the controller having a timer that measures the frequency separation and a control output feeding back to the control input that stabilizes the first separation as a function of timing the evaluation outputs; the controller further having a correction circuit that corrects the level output as a function of the first frequency separation.

2. The radar gauge of Claim 1, further comprising:

- a divider circuit dividing the first and second clock frequencies and generating the transmit and the sample frequencies wherein the transmit and sample frequencies are separated from each other by a second

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frequency separation; and
the separation sensing circuit further coupling
to the divider circuit and generating a
second evaluation output coupling to the
controller as a function of the second
frequency separation.

3. The radar gauge of Claim 2 wherein the separation
sensing circuit further comprises:

a circuit sensing a polarity of the sample clock
and generating a further evaluation output
representative of the polarity.

4. The radar gauge of Claim 1 wherein the clock
source comprises a voltage controlled oscillator
controlled by the control output and generating the
second clock frequency.

5. The radar gauge of Claim 4 wherein the controller
comprises a digital-to-analog converter generating the
control output.

6. The radar gauge of Claim 1 wherein the controller
includes a timer measuring time intervals of an
evaluation output.

7. The radar gauge of Claim 6 wherein the level
output includes a current calculated distance that is
a function of a current timer measurement.

8. The radar gauge of Claim 1 wherein the controller
includes a timer performing a timer measurement of a
count an evaluation output during a time interval.

9. The radar gauge of Claim 8 wherein the level output includes a current calculated distance that is a function of a current timer measurement.

10. The radar gauge of Claim 1 wherein the radar gauge circuit includes a transmit pulse generator and a sample pulse generator controlled respectively by the transmit clock and the sample clock.

11. The radar gauge of Claim 1 wherein the radar gauge is energized solely by a 4-20 mA analog current and includes a voltage regulator energized by the 4-20 mA analog current.

12. A method of stabilizing clock generation in a radar gauge adapted to sense fluid level in a tank, comprising:

generating first and second clock frequencies separated from each other by a first frequency separation controlled by a control input;

generating a first evaluation output as a function of the first frequency separation;

generating a control output feeding back to the control input that stabilizes the first separation as a function of the evaluation output; and

generating a level output as a function of the stabilized first frequency separation, the level output corrected as a function of the first frequency separation .

13. The method of Claim 12, further comprising:
dividing the first and second clock frequencies
to generate the transmit and sample
frequencies separated from each other by a
second frequency separation; and
generating a second evaluation output as a
function of the second frequency separation;
and
generating the control output as a further
function of the second evaluation output
and
correcting the level output as a function of the
second evaluation output.
14. The method of Claim 12 further comprising:
sensing a polarity of the sample clock and
generating a further evaluation output
representative of the polarity.
15. The method of Claim 12 further comprising:
generating the second clock frequency in a
voltage controlled oscillator wherein an
oscillator control voltage is controlled by
the control output.
16. The method of Claim 15 further comprising:
generating the oscillator control voltage in a
digital-to-analog converter.
17. A computer-readable medium having stored thereon
a plurality of sequences of instructions for execution
by a processor in a radar gauge adapted to sense fluid
level in a tank, the radar gauge adapted to transmit

and receive radar transmissions, the plurality of sequences of instructions including sequences, when executed, that cause the processor to perform the sequence:

receiving a first evaluation output representing a first frequency difference between first and second clock frequencies generated by a clock source having a control input;

receiving a second evaluation output representing a second frequency difference between transmit and sample frequencies generated by dividing the first and second frequencies respectively; and

generating a control output feeding back to the control input that stabilizes the first separation as a function of the first and second evaluation outputs.

18. The computer readable medium of Claim 17 wherein the sequence of instructions further comprises:

sensing a polarity of the sample clock and generating a further evaluation output representative of the polarity.

19. A radar gauge adapted to sense fluid level in a tank, comprising:

means for receiving a transmit frequency and a sample frequency controlling radar transmission and level sampling respectively, and for generating a level output;

means for generating first and second clock frequencies separated from each other by a

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first frequency separation, the clock source having a control input setting the first separation;

means for dividing the first and second clock frequencies and for generating the transmit and sample clock frequencies separated from each other by a second frequency separation; means for sensing the first and second frequency separations and generating evaluation outputs as functions of the first and second frequency separations; and

means for controlling a control output feeding back to the control input, stabilizing the first separation as a function of the evaluation outputs.

20. The radar gauge of Claim 19, further comprising:
means for sensing a polarity of the sample clock and generating a further evaluation output representative of the polarity.

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STABILIZATION IN A RADAR LEVEL GAUGE

ABSTRACT OF THE DISCLOSURE

A radar gauge adapted to sense fluid level in a tank and including a radar gauge circuit in which radar transmission and level sampling are controlled by a transmit frequency and a sample frequency respectively. A first frequency separation between first and second frequencies is controlled by a control input. The first and second frequencies can be divided to generate the transmit and sample frequencies, separated by a second frequency separation. At least one frequency difference is evaluated and the evaluation used to generate the control input, stabilizing the first frequency difference, and to correct the gauge output.

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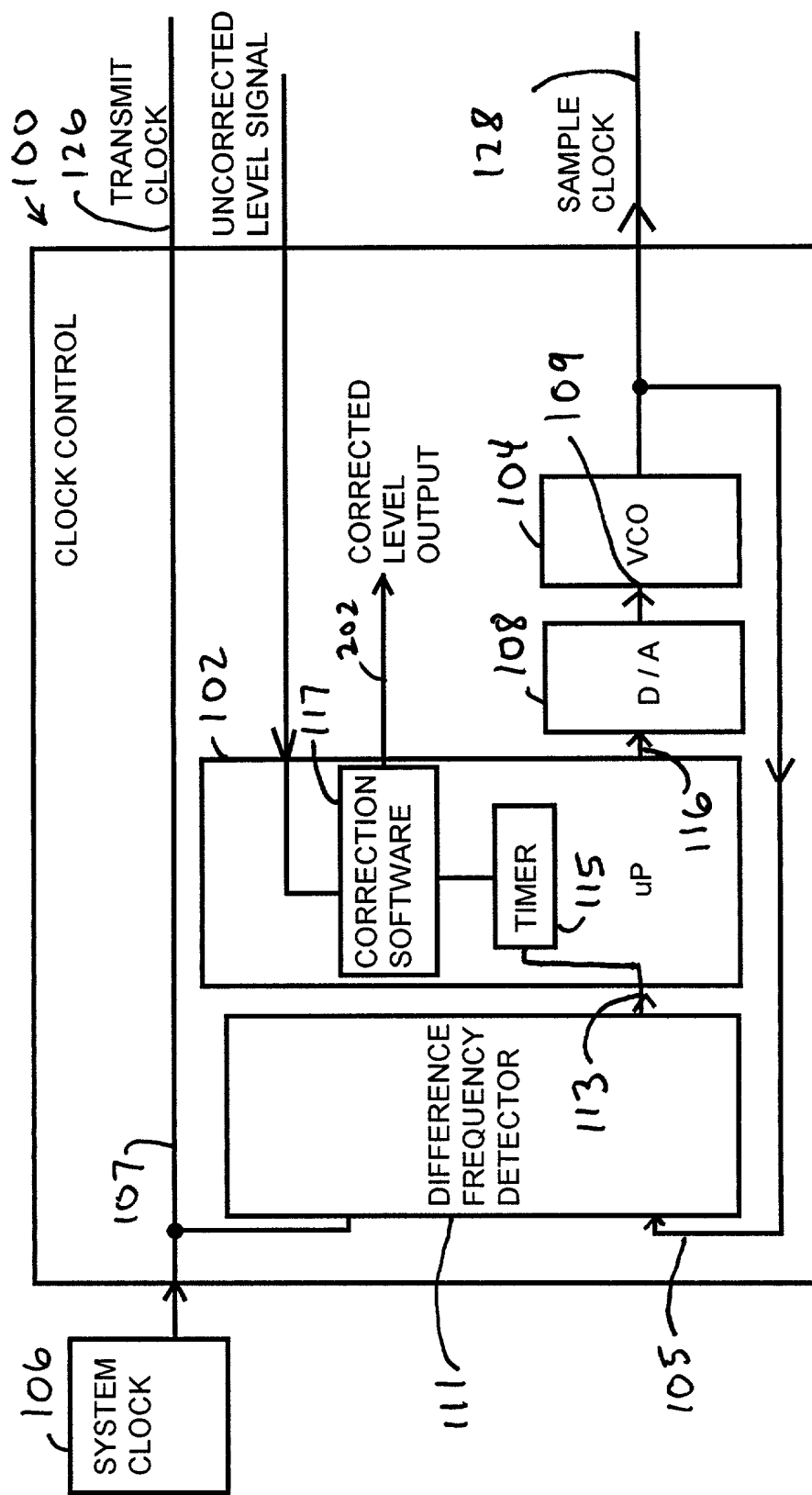
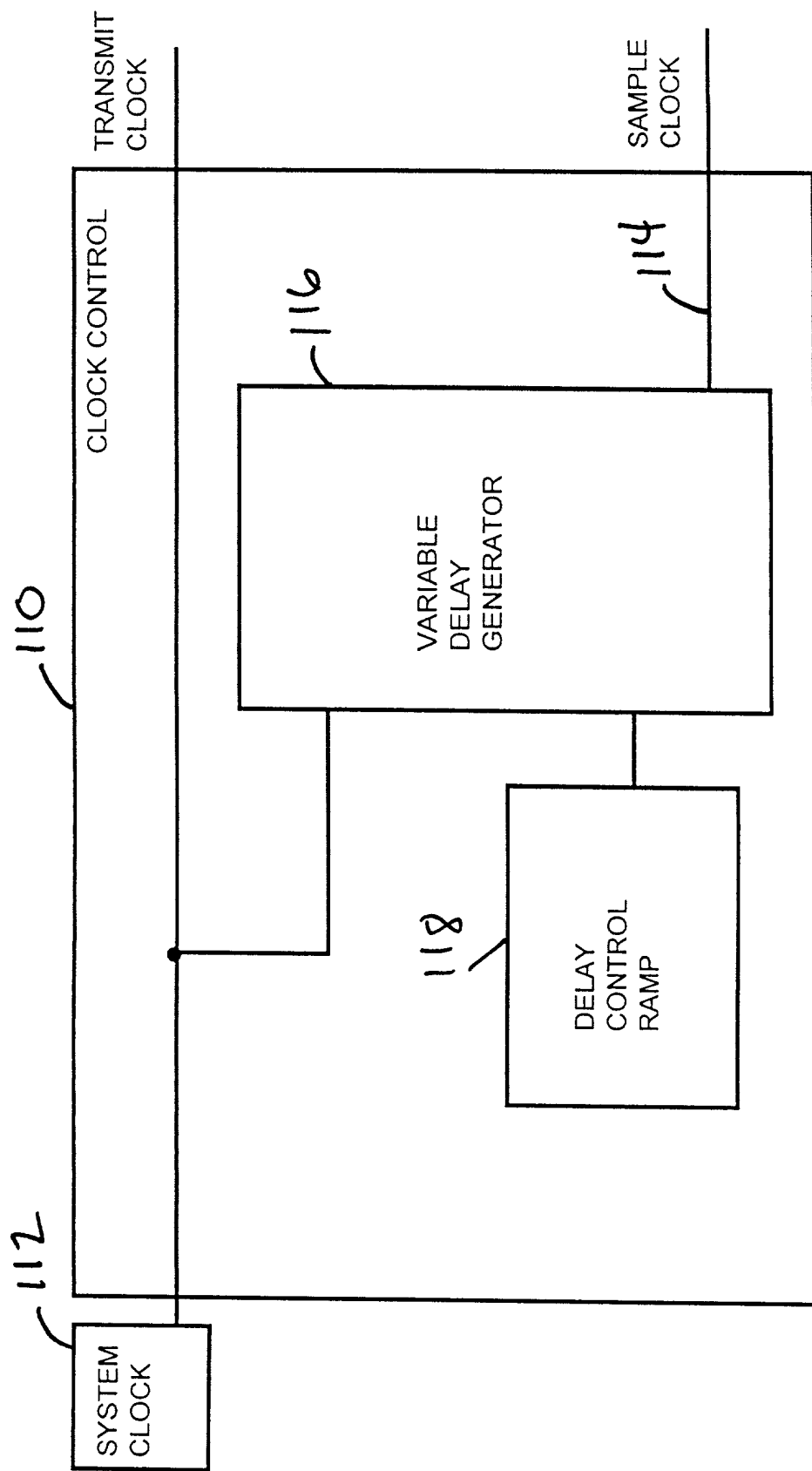


FIG. 1



PRIOR ART

FIG. 2

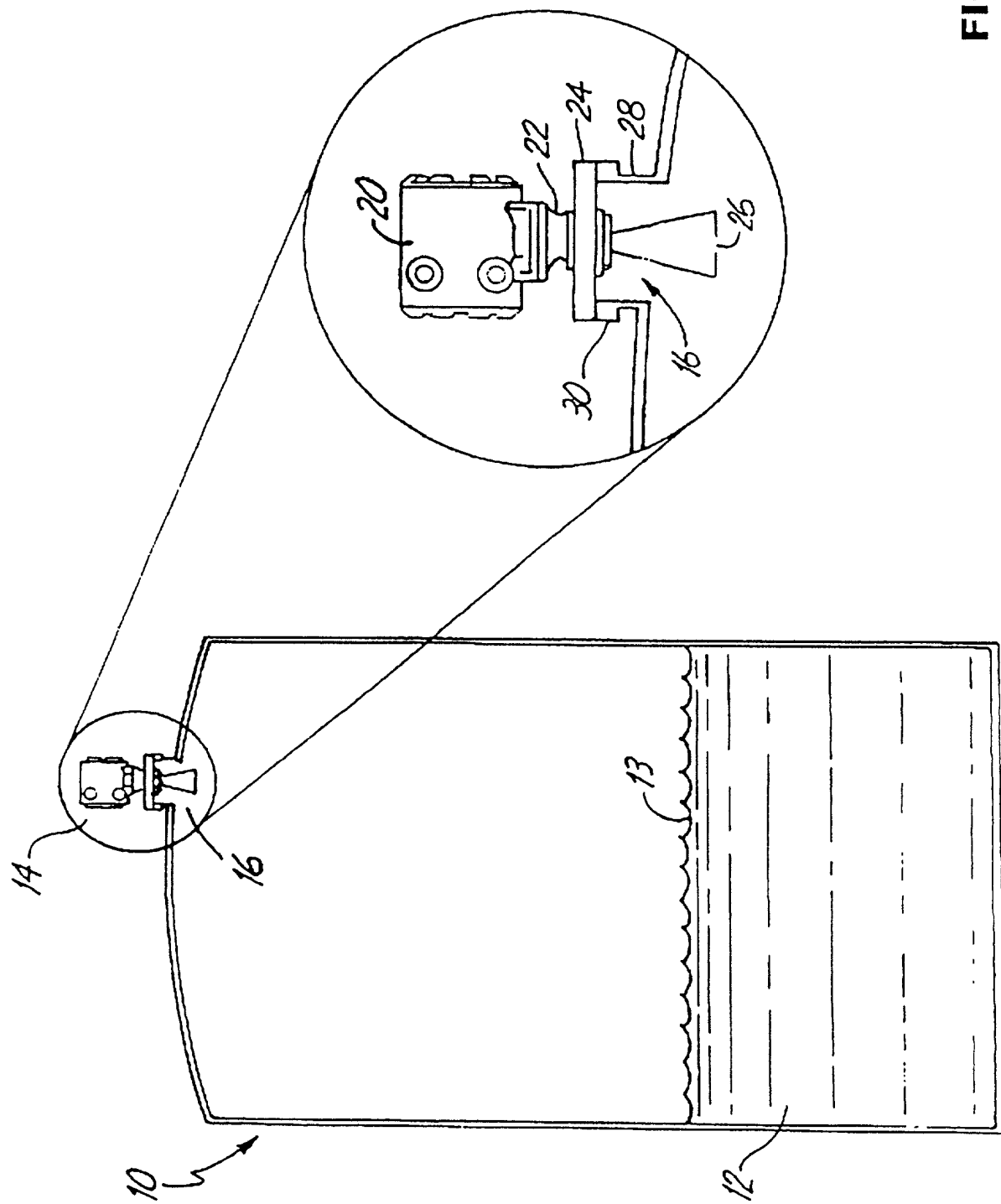


FIG.3

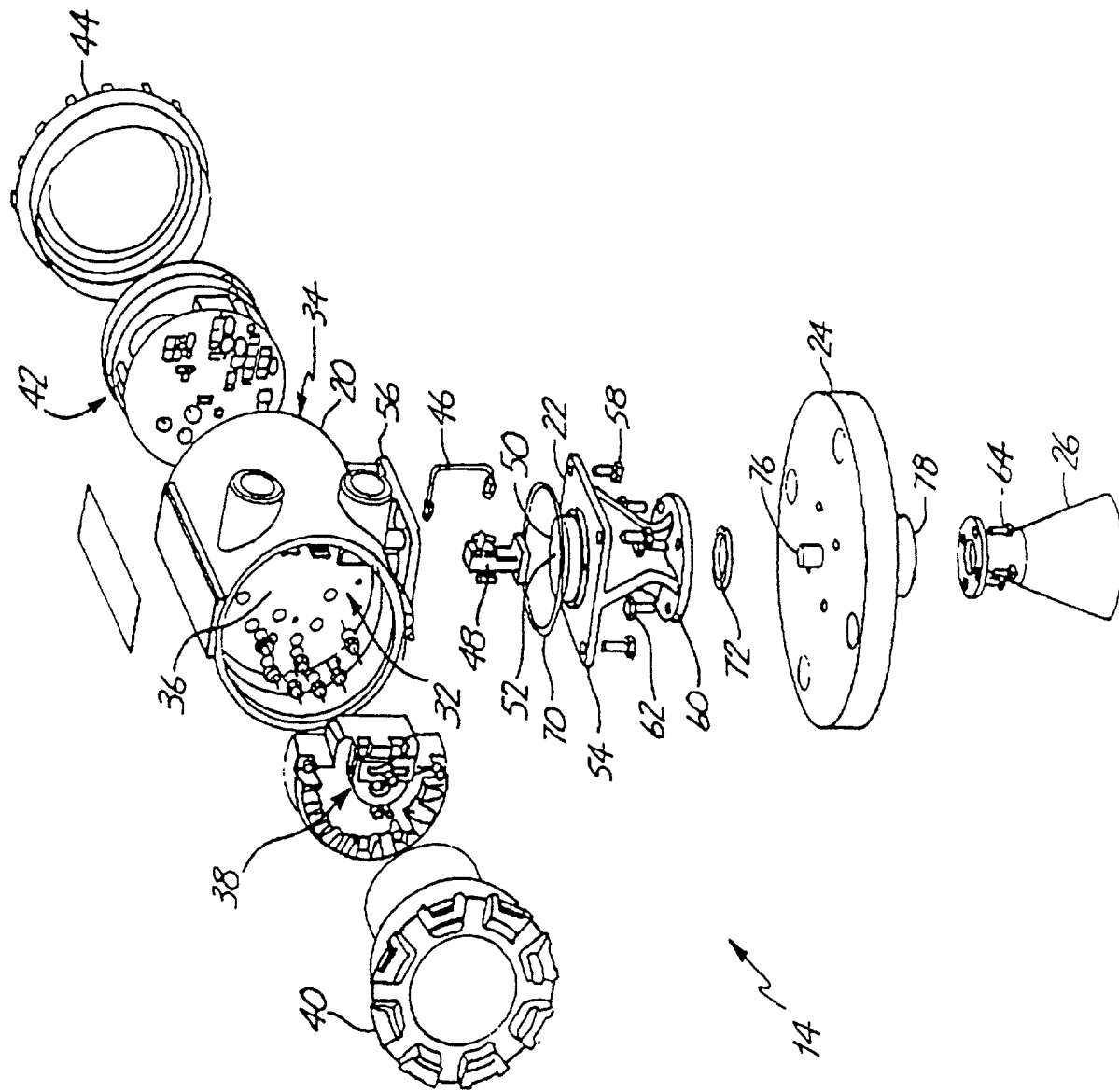


FIG. 4

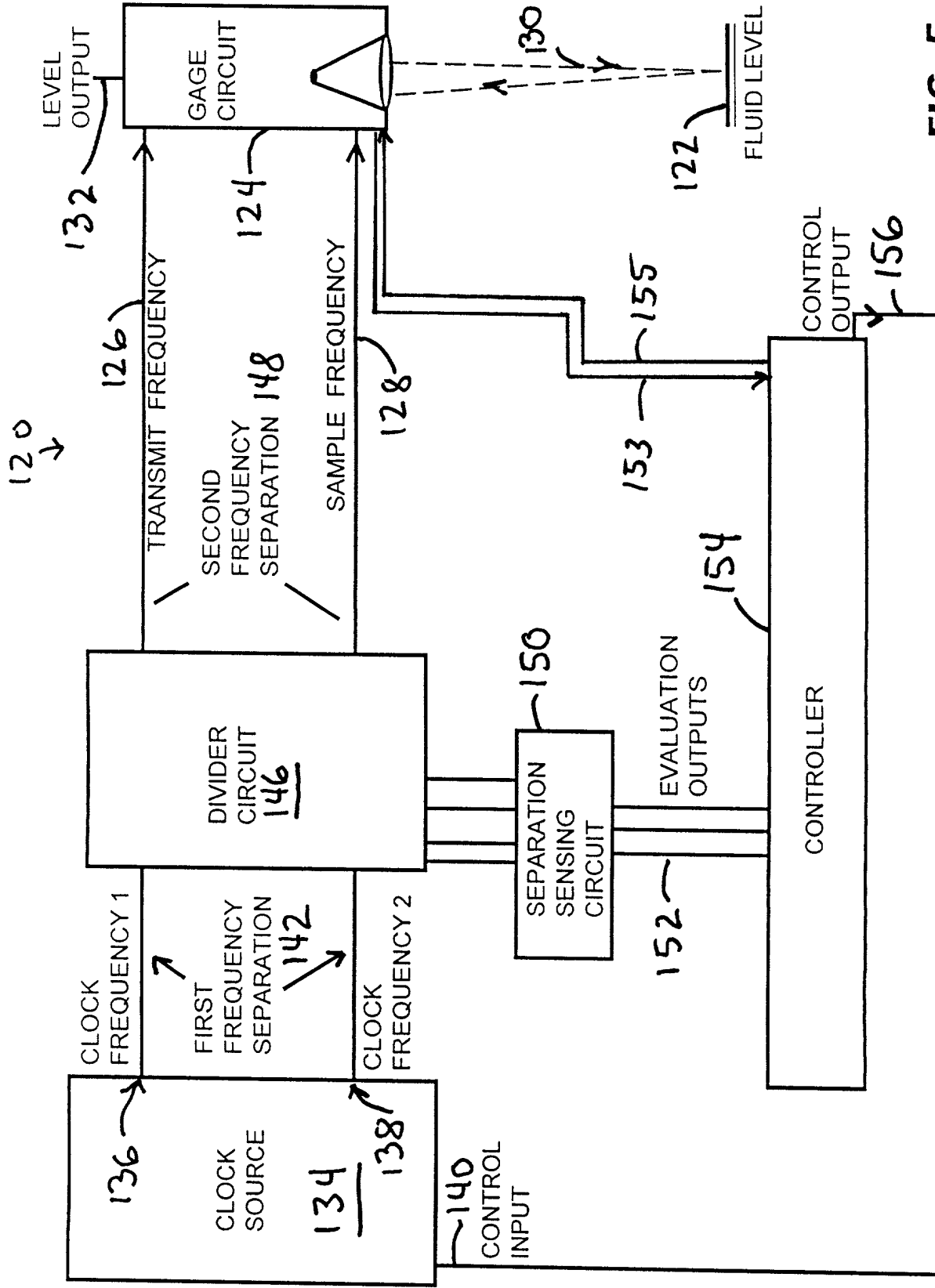


FIG. 5

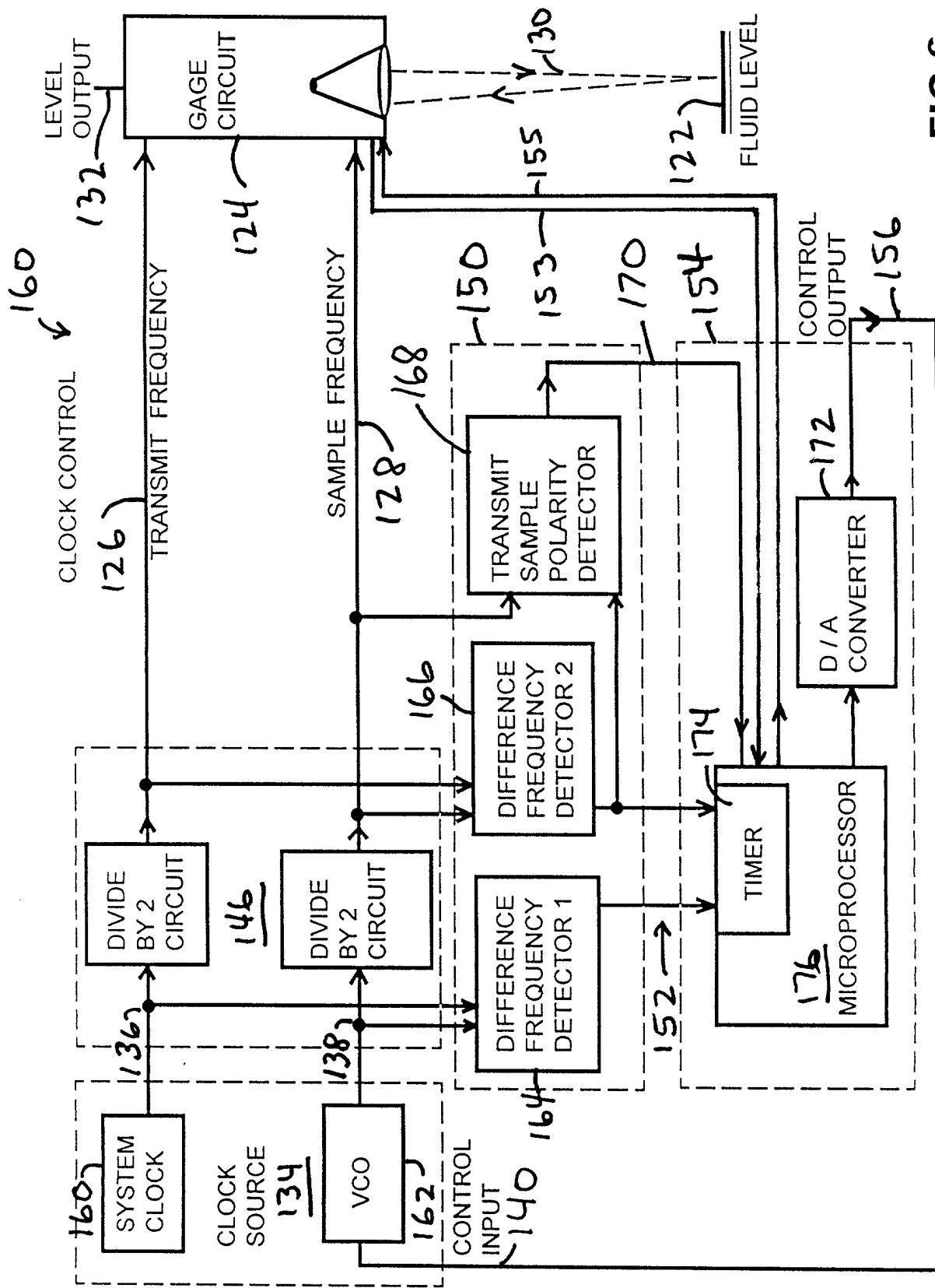


FIG. 6

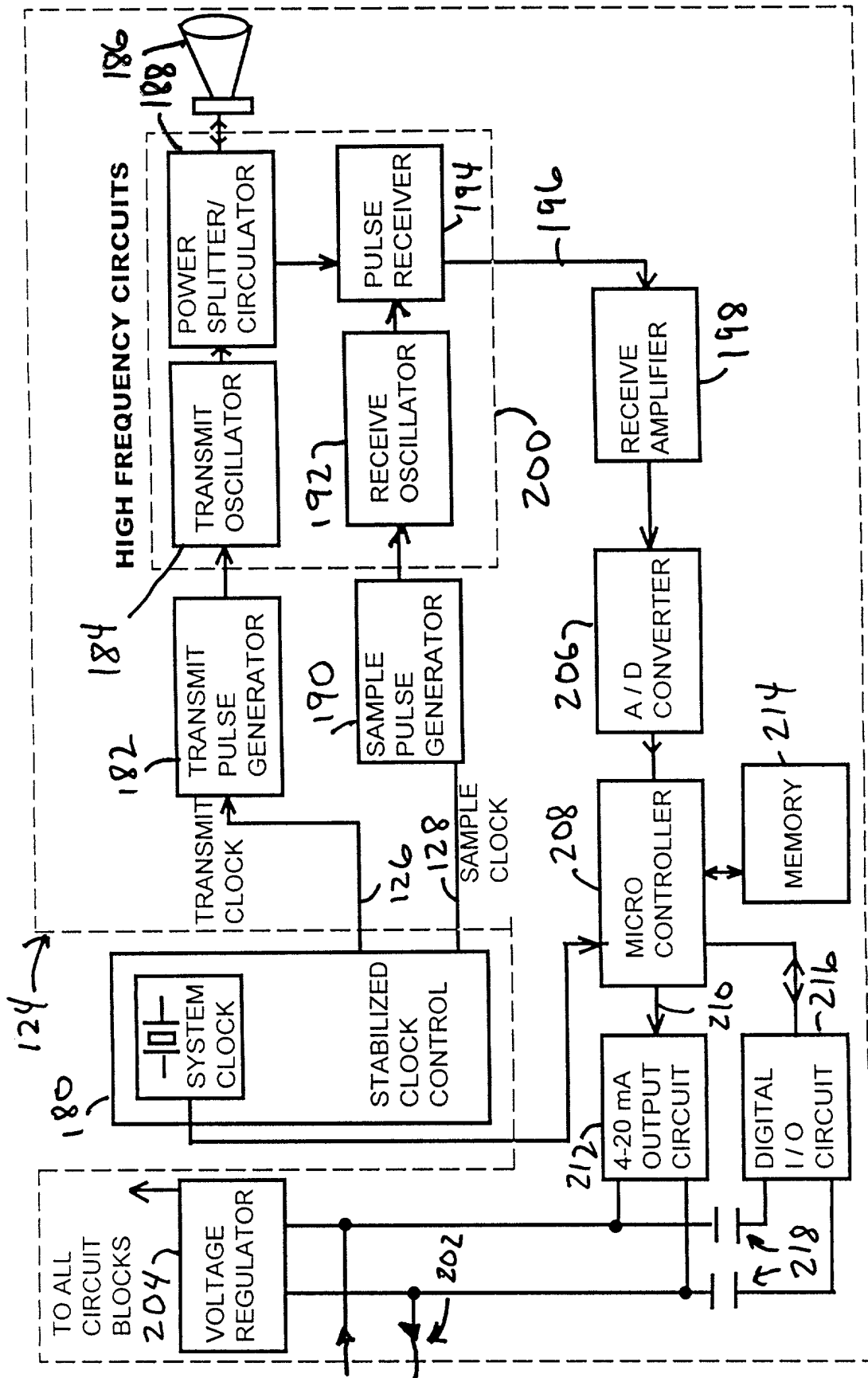


FIG. 7

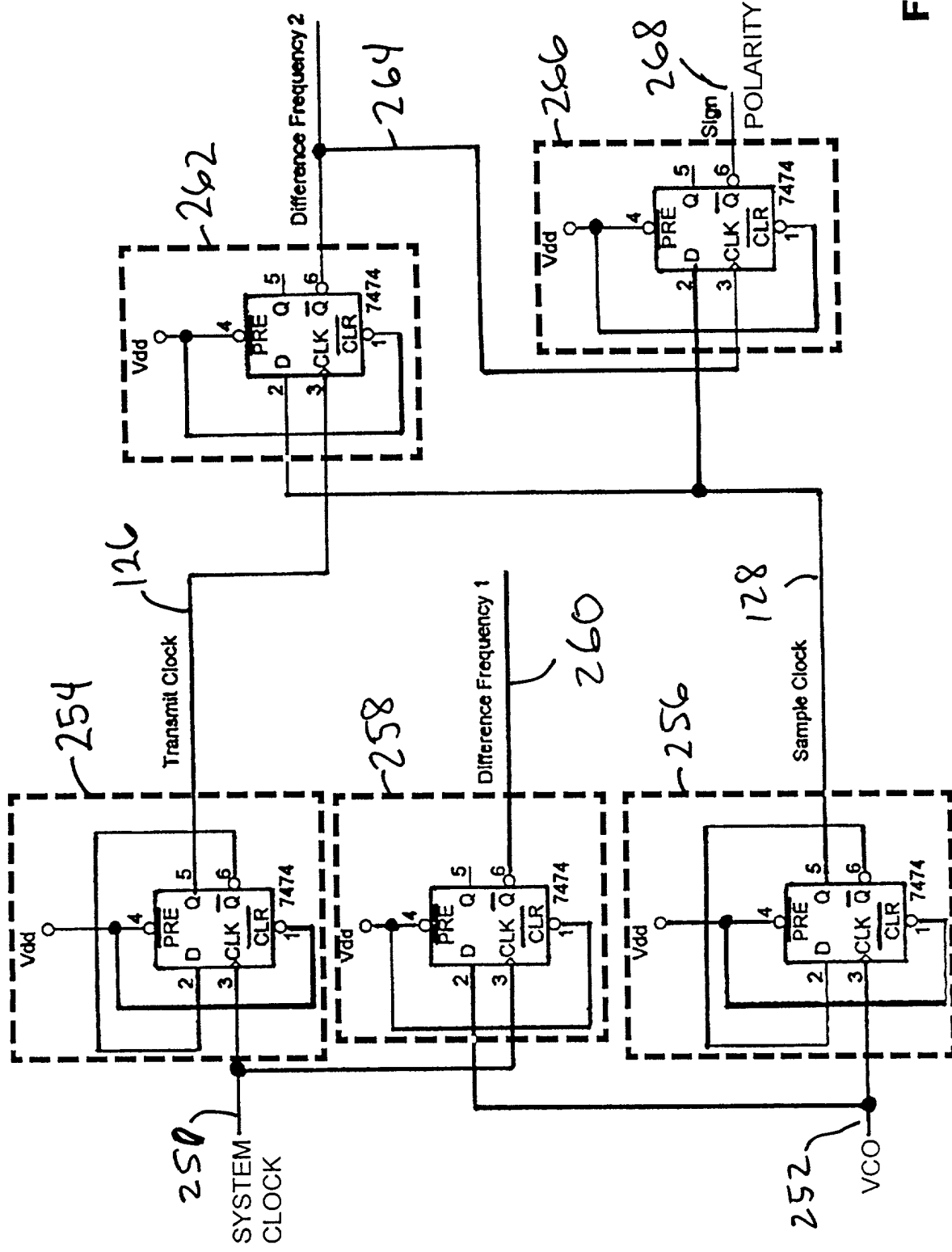


FIG.8

COMBINED DECLARATION AND
POWER OF ATTORNEY
IN ORIGINAL APPLICATION

Attorney Docket No.

R11.12-0662

SPECIFICATION AND INVENTORSHIP IDENTIFICATION

As a below named inventor, I declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed, and for which a patent is sought, on the invention entitled STABILIZATION IN A RADAR LEVEL GAUGE the specification of which,

(check one) X is attached hereto.

— was filed on as Appln. Serial No. .

— and was amended on .

— was described and claimed in PCT International Application

No. filed on and as amended under PCT Article

19 on .

ACKNOWLEDGEMENT OF REVIEW OF PAPERS AND DUTY OF CANDOR

I have reviewed and understand the contents of the above identified specification, including the claims, as amended by all amendment referred to above. I acknowledge the duty to disclose information which is known to me to be material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, § 1.56.

PRIORITY CLAIM (35 USC § 120)

I claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below. Insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35 United States Code § 112, I acknowledge the duty to disclose to the Patent Office all information known to me to be material to patentability as defined in Title 37 Code of Federal Regulations § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Appln. Ser. No.	U.S. Serial No. (if any under PCT)	Filing Date	Status
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_____	_____	_____	_____
_____	_____	_____	_____

DECLARATION

I declare that all statements made herein that are of my own knowledge are true and that all statements that are made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby appoint the following attorneys and/or agents to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith: Eric R. Waldkoetter, Reg. No. 36,713; Rudolph P. Hofmann, Jr., Reg. No. 38,187; Nickolas E. Westman, Reg. No. 20,147; Joseph R. Kelly, Reg. No. 34,847; and Judson K. Champlin, Reg. No. 34,797; Steven M. Koehler, Reg. No. 36,188; David D. Brush, Reg. No. 34,557; John D. Veldhuis-Kroeze, Reg. No. 38,354; Deirdre Megley Kvale, Reg. No. 35,612; Theodore M. Magee, Reg. No. 39,758; Peter S. Dardi, Reg. No. 39,650; Christopher R. Christenson, Reg. No. 42,413; and John A. Wiberg, Reg. No. P-44,401.

I ratify all prior actions taken by Westman, Champlin & Kelly, P.A. or the attorneys and agents mentioned above in connection with the prosecution of the above-mentioned patent application.

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